

processors*Ravi Bhargava; John, L.K.;*

Computer Architecture, 2003. Proceedings. 30th Annual International Symposium , 9-11 June 2003

Pages:264 - 274

[\[Abstract\]](#) [\[PDF Full-Text \(295 KB\)\]](#) [IEEE CNF](#)**6 Generation uncertainty in long range transmission planning***Miller, N.W.;*

Power Engineering Society Summer Meeting, 2002 IEEE , Volume: 3 , 21-25 J 2002

Pages:1038 - 1040 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(471 KB\)\]](#) [IEEE CNF](#)**7 Cherry: Checkpointed early resource recycling in out-of-order microprocessors***Martinez, J.F.; Renau, J.; Huang, M.C.; Prvulovic, M.;*

Microarchitecture, 2002. (MICRO-35). Proceedings. 35th Annual IEEE/ACM International Symposium on , 18-22 Nov. 2002

Pages:3 - 14

[\[Abstract\]](#) [\[PDF Full-Text \(349 KB\)\]](#) [IEEE CNF](#)**8 A rapid prototyping methodology for reverse engineering of legacy electronic systems***Deno, S.; Landis, D.; Hulina, P.; Balasubramanian, S.;*

Rapid System Prototyping, 1999. IEEE International Workshop on , 16-18 Jun 1999

Pages:222 - 227

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) [IEEE CNF](#)

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